SYNTHESIS OF THE K-MATRIX OF (n+1)-NODE RESISTIVE n-PORT NETWORKS

P. Subbarami Reddy and K. Thulasiraman* Department of Electrical Engineering Indian Institute of Technology Madras, India

Abstract

In this paper a new procedure for the synthesis of the K-matrix of an (n+1)-node resistive n-port network is given.

1.INTRODUCTION

Recently Lempel and Cederbaum have considered the synthesis of the K-matrices of resistive n-port networks.Synthesis of the K-matrix essentially requires the solution of the following two problems:

- Determination of the port configuration T appropriate to the given Kmatrix.
- (2) Determination of an n-port network containing no negative conductances and having the specified K-matrix.

Lempel and Cederbaum have provided a solution to both the problems. In a companion paper [2] an alternate approach is suggested to solve the second problem using the concepts of padding network and network of departure.

In their solution of the first problem Lempel and Cederbaum first assume the port configuration to be in n parts and obtain the modified cut-set matrix, appropriate to the assumed port configuration and the given K-matrix by making use of a matrix equation relating the modified cut-set matrix and the K-matrix. From the modified cut-set matrix so obtained, the port configuration is determined by the application of a simple procedure. Though this procedure yields a unique port configuration T for a given K-matrix, in some cases, the port configuration T is found, as pointed out later in this paper, to be inconsistent with the matrix. In such cases, the inconsistency will be revealed after a time consuming process of linear programming employed to solve the second problem. Hence, a new procedure for the determination of the port configuration appropriate to a given K-matrix is called for.

Consider a resistive n-port network N having no internal vertices. Let the port configuration T of N be in p parts, T_i , i = 1, 2, ..., p. If no virtual short-circuits are present in N, then the K-matrix of N can be partitioned as

	^к 11	^K 12 ·	•	•	Klp
	K ₂₁	к ₂₂ .	٠	•	κ _{2p}
$K = [k_{ij}] =$			٠	•	• •
	K _{p1}	K _{p2} .	•	•	к

where

 (1) the rows and columns of the submatrix K_{ij}, i ≠ j correspond to the ports in T_i and T_j respectively; 1000

- (2) each submatrix K_{ii} has entries comprising of 1's and 0's only and is uniquely determined by T_i;
- (3) all diagonal entries of K are equal to unity; and

K. Thulasiraman is presently a Post-Doctoral Fellow at Sir George Williams University for 1970-72, on leave of absence from IIT, Madras.

(4) Each entry in $K_{j,j}$, $j \neq 0$ is less than or equal to unity and greater than or equal to zero, except in degenerat cases.

From the above it may be seen that the problem of determining the port configuration $T = T_1 UT_2 U...T_p$ appropriate to the Kmatrix of a resistive n-port network having more than (n+1) nodes is equivalent to the synthesis of the K-matrix of (n+1)node n-port networks. Hence we consider in this paper the synthesis of the K-matrix of (n+1)-node n-port networks.

II. SYNTHESIS OF THE K-MATRIX OF (n+1)-NODE n-PORT NETWORKS

Consider an (n+1)-node n-port network N. Let T be the port configuration and K, the potential factor matrix of N. It is assumed that each port edge is oriented toward the negative reference terminal. The following theorems can be proved easily using the definition for k₁'s, the potential factors [3].

Theorem 1. Row i of K corresponds to a tip-port if and only if all k_{ij} 's, $j \neq i$ are equal.

Theorem 2. A tip-port is oriented toward $\overline{(away \ From)}$ the tip-vertex at which it is incident, if and only if all k_{ij} 's are equal to unity (zero).

Let j and m be any two tip-ports in T. Let the unique path in T containing the ports j and m be denoted by P_{im} .

Theorem 3. Any non-tip-port x of T will be in P_{in} if and only if $k_{xj} = 0$ and $k_{xm} = 1^{jm}$ vice versa.

Theorem 4. There exists a path containing the terminals of port j and the negative (positive) reference terminal of port i and not containing the positive (negative) reference terminal of port i, if and only if $k_{ij} = 0$ (1).

Based on the above theorems, we now give a procedure for the determination of the port configuration T appropriate to the K-matrix of an (n+1)-node resistive n-port network.

Step 1. Identify all the tip ports of T using Theorem 1.

Step 2. Determine the orientations of the tip-ports using Theorem 2.

Step 3. Let S denote the set of all tipports. Let a port j be in S. Determine all P_{im} 's, m ε S, using theorems 3 and 4.

Step 4. Use the paths P_{ij} 's to determine the relative locations of the ports in T

and hence T.

Step 5. If the potential factor matrix corresponding to this port configuration T is equal to K, then T is the required port configuration; otherwise K is not realizable.

Example 1. Consider the matrix

	0	1	[٥
К =	0	0	1
	1	0	0]

This matrix has been used in Example 5 of [2].

It may be observed that in this matrix, in no row all the off-diagonal entries are equal. Hence, by Theorem 1, there exists no row which corresponds to a tip port. However, for any T, there must be at least two tip-ports. Hence the matrix can not be realized as the K-matrix of an (n+1)-node resistive n-port network. This is shown to be true in [2] after solving a linear program.

Example 2. Consider the matrix given by

	1	0	0	0	٦٥
	0	1	1	1	1
К =	0	0	1	0	0
	0	0	0	1	1
	11	1	1	1	1

It is required to determine the port-configuration of the 5-port network for which K is the potential factor matrix.

Step 1. Using Theorem 1, ports 1,3 and 5 are identified as the tip-ports.

Step 2. Ports 1 and 3 are oriented away from the respective tip vertices and port 5 is oriented towards the tip-vertex.

Step 3. If E₁ denotes the set of all ports $\overline{\ln P_{ij}}$ then using Theorem 3, we can determine E₁₃ and E₁₅ as follows: $1_{13}^{2} = \{1,2,3\}, E_{15}^{2} = \{1,2,4,5\}$ $1_{13}^{2} = \{1,2,3\}, E_{15}^{2} = \{1,2,4,5\}$

Using Theorem 4, P_{13} and P_{15} are determined and are shown in Fig. 1. The required port configuration T is shown in Fig. 2.

The potential factor matrix corresponding to T is seen to be equal to the given matrix K. Hence T is the required port-configuration.

III. CONCLUSIONS

The procedure given by Lempel and Cederbaum to determine the port-configuration corresponding to a given matrix K is unique. However in certain cases, as pointed out in Example 1, the port-configuration may not be consistent with the given K-matrix. This inconsistency will be revealed only after a time consuming process of solving a linear program. The procedure given in this paper enables one to detect the nonrealizability of the matrices quite early in the realization procedure thereby obviating the need to solve a linear program.

Acknowledgement. This work is part of a doctoral thesis of P. Subbarami Reddy to be submitted to The Indian Institute of Technology, Madras, India.

۱

ļ

References

- Lempel, A. and Cederbaum, I., "K-matrix of n-port networks - analysis and synthesis," IEEE Trans. on Circuit Theory, Vol. CT-16, pp. 471-483, Nov. 1969.
- 2. P. Subbarami Reddy and K. Thulasiraman, "Analysis and synthesis of the K and Ymatrices of resistive n-port networks having more than (n+1)-nodes," companion paper available in this volume.
- Thulasiraman, K. and Murti, V.G.K., "Modified cutset matrix of an n-port network," Proc. IEE, Vol. 115, No. 9, pp. 1263-1268, Sept. 1968.







Fig 2