Last Time

• Memory design
Today

• Finite state machines:
  – Model of sequential behavior
  – As applied to control

• Lab 2 specification
Administrivia

- Project 1 due today
  - Reports by 5:00pm
- Homework 2 hand-in:
  - See Mark’s announcement on blackboard
- Quiz 1:
  - Available for pickup now
Homework 1

• Pick up graded assignments from Mark any time after class

• Questions about solution set now?
Finite State Machines (FSMs)

Pure FSM form is composed of:

• A set of states
• A set of possible inputs (or events)
• A set of possible outputs
• A transition function:
  – Given the current state and an input: defines the output and the next state
Finite State Machines (FSMs)

States:

• Represent all possible “situations” that must be distinguished

• At any given time, the system is in exactly one of the states

• There is a finite number of these states
Finite State Machines (FSMs)

An example: our synchronous counter
• States: ?
Finite State Machines (FSMs)

An example: our synchronous counter

• States: the different combinations of the digits: 000, 001, 010, … 111

• Inputs: ?
Finite State Machines (FSMs)

An example: our synchronous counter

• Inputs:
  – Really only one: the event associated with the clock transitioning from high to low
  – We will call this “C”

• Outputs: ?
Finite State Machines (FSMs)

An example: our synchronous counter
• Outputs: same as the set of states
• Transition function: ?
Finite State Machines (FSMs)

An example: our synchronous counter

• Transition function:
  – On the clock event, transition to the next state in the sequence
FSM Example: Synchronous Counter

A Graphical Representation:

A set of states
FSM Example: Synchronous Counter

A transition

C/001

000 → 001

010

011

100

111

110

101

Andrew H. Fagg: Embedded Real-Time Systems: FSMs
FSM Example: Synchronous Counter

A transition

The event

Andrew H. Fagg: Embedded Real-Time Systems: FSMs
FSM Example: Synchronous Counter

A transition

The output
FSM Example: Synchronous Counter

The next transition

The diagram shows a state transition diagram for a synchronous counter with states labeled 000, 001, 010, 011, 100, 101, 110, and 111. The transitions are labeled with 'C/001' and 'C/010'.
FSM Example: Synchronous Counter

The next transition

C/010 001 -> 010

C/011 010 -> 011

C/001 000 -> 001

C/001 011 -> 010

101

110

111
FSM Example: Synchronous Counter

The full transition set

Diagram showing the full transition set for a synchronous counter with states 000, 001, 010, 011, 100, 101, 110, 111, with transitions labeled C/000, C/001, C/010, C/011, C/100, C/101, C/110, C/111.
FSM Example: Synchronous Counter

Initial condition

- Initial state: 000
- Transitions:
  - C/000: 000 to 001
  - C/001: 000 to 010
  - C/010: 001 to 010
  - C/011: 001 to 011
  - C/101: 011 to 100
  - C/000: 010 to 010
  - C/100: 010 to 100
  - C/110: 011 to 110
  - C/111: 011 to 111
  - C/101: 100 to 101
  - C/111: 110 to 111
  - C/110: 111 to 000

Note: x/000 indicates the initial condition set to 000.
Example II: An Up/Down Counter

Suppose we have two events (instead of one): Up and down

• How does this change our state transition diagram?
Example II: An Up/Down Counter

From state 000, there are now two possible transitions

- From state 000, there is a transition to state 001 when the input is U/001.
- From state 000, there is a transition to state 111 when the input is D/111.

The next states are:
- 001
- 010
- 011
- 100
- 101
- 110
- 111
Example II: An Up/Down Counter

Likewise for state 001…

000 -> 001
001 -> 010
010 -> 100
d/111 -> 110
d/000 -> 011
u/001 -> 001
u/010 -> 010

Example II: An Up/Down Counter

The full transition set

```
U/000  D/000  D/001  U/001

U/001  D/000  D/001  U/001

U/010  D/010  D/011  U/011

U/100  D/100  D/101  U/101

U/110  D/110  D/111  U/111
```

Andrew H. Fagg: Embedded
Real-Time Systems: FSMs
Last Time

Finite State Machines are defined by:

• A set of states
• A set of inputs (or events)
• A set of outputs
• A state transition function
• An output function
Today

• FSMs for control
• Implementing FSMs in code
• Lab 2
Administrivia

- HW 2 due today
- Lab 2 is now available
- HW 1 and quiz 1 are available for pick up

• Reading:
  – FSM web pages
  – Atmel Mega8 processor summary
  – ESP 9,10
FSMs and Control

How do we relate FSMs to Control?
• States are?
FSMs and Control

How do we relate FSMs to Control?
• States are our memory of recent inputs

• Inputs are ?
FSMs and Control

How do we relate FSMs to Control?

- States are our memory of recent inputs

- Inputs are some processed representation of what the sensors are observing

- Outputs are?
FSMs and Control

How do we relate FSMs to Control?

• States are our memory of recent inputs

• Inputs are some processed representation of what the sensors are observing

• Outputs are the control actions
FSMs: A Control Example

Suppose we have a vending machine:

• Accepts dimes and nickels
• Will dispense one of two things once $.20 has been entered: Jolt or Buzz Water
  – The “user” requests one of these by pressing a button
• Ignores select if < $.20 has been entered
• Immediately returns any coins above $.20
Vending Machine FSM

What are the states?
Vending Machine FSM

What are the states?

- $0
- $.05
- $.10
- $.15
- $.20
Vending Machine FSM

What are the inputs/events?
Vending Machine FSM

What are the inputs/events?

• Input nickel (N)
• Input dime (D)
• Select Jolt (J)
• Select Buzz Water (BW)
Vending Machine FSM

What are the outputs?
Vending Machine FSM

What are the outputs?
• Return nickel (RN)
• Return dime (RD)
• Dispense Jolt (DJ)
• Dispense Buzz Water (DBW)
• Nothing (Z)
Vending Machine Design

What is the initial state?
Vending Machine Design

What is the initial state?
• $S = 0$
Vending Machine Design

What can happen from $S = \$0$?

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<tr>
<th>Event</th>
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<th>Output</th>
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Vending Machine Design

What can happen from $S = \$0$?

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<td>D</td>
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<td>J</td>
<td>$0</td>
<td>Z</td>
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<tr>
<td>BW</td>
<td>$0</td>
<td>Z</td>
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</table>

What does this part of the diagram look like?
Vending Machine Design

A piece of the state diagram:

- $0$
- $.05$
- $.10$
- x/Z
- N/Z
- J/Z
- BW/Z
- D/Z
### Vending Machine Design

What can happen from $S = $0.05?

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Vending Machine Design

What can happen from $S = $0.05? What does the modified diagram look like?

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<td>Z</td>
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<tr>
<td>BW</td>
<td>$.05</td>
<td>Z</td>
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</tbody>
</table>
Vending Machine Design

A piece of the state diagram:

```
x/Z  J/Z  BW/Z

$0  N/Z  $0.05  D/Z  $0.15

J/Z  BW/Z

$0.10  N/Z  $0.15
```
Vending Machine Design

What can happen from $S = \$0.10$?

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Vending Machine Design

What can happen from $S = $0.10?  

<table>
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<tr>
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<tbody>
<tr>
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Vending Machine Design

A piece of the state diagram:
Vending Machine Design

What can happen from \( S = \$0.15 \)?

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Vending Machine Design

What can happen from $S = $0.15? 

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<tr>
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Vending Machine Design

A piece of the state diagram:
Finally: what can happen from $S = 0.20$?

<table>
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</table>
Finally, what can happen from S = $0.20?

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<tr>
<td>BW</td>
<td>$0</td>
<td>DBW</td>
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Vending Machine Design

The complete state diagram:
Project 2: The Problem

Project 1:
• Implementation of a feedback control circuit (in digital logic) that orients and then moves toward a beacon

Project 2:
• Integrate this capability into a sequence of movements
Project 2: The Problem

Primary behavior of the robot:

• Phase 1:
  – Move toward beacon in front of the robot
  – Scan for another beacon on the left
  – When beacon is found, turn toward it

• Phase 2:
  – Move toward beacon in front
  – Scan for another beacon on the right
  – When beacon is found, turn toward it

• Repeat
Project 2: The Problem

An exception occurs if the robot loses sight of the forward beacon (no signal on either the left or the right sensor pair)

If in phase 1:

• Rotate turret to the right
• If a beacon is found, then turn the robot toward it and continue with phase 2
• Else stop moving
Project 2: The Problem

Exception handling

If in phase 2:
• Rotate turret to the left
• If a beacon is found, then turn the robot toward it and continue with phase 1
• Else stop moving
Project 2: Step 1

Design the FSM for this problem
• What are the states?
• What are the sensory signals?
• What are the inputs?
• What are the outputs?
Project 2: Step 2

Design the FSM for this problem

• What is the mapping from sensory signals to events?
Project 2: Step 3

Design the FSM for this problem
• What does the transition function look like?
Project 2: Step 4

Design the FSM for this problem

• What is the mapping from output to robot action?
• What must the robot do if no event occurs?
Project 2: Step 5

Implementation
• Write a C program that implements your FSM
• Burn this onto an Atmel mega8 processor
• Get it to work!
Next Time

• Another FSM control example
• Practicalities of Atmel processor programming
• Reading:
  – Lab 2 description
  – Atmel mega8 summary
  – The Atmel programming HOWTO
Implementing Finite State Machines

How would we implement an FSM with the logic components we have studied so far?
Implementing Finite State Machines

• Use flip-flops to represent the states:
  – Each FSM state is represented as a unique combination of flip-flop states
  – A clock signal is distributed to each flip-flop

• Use a set of binary input lines to represent the FSM inputs
  – We represent a FSM input with a unique combination of input bits
Implementing Finite State Machines

• The **transition function** is implemented as a logic function:
  – Combines the inputs with the current state
  – Produces the next state for each of the flip-flops

• **Output:**
  – Another logic function of the flip-flop states
Implementing FSMs in Code

Representing the current state:
• Introduce a “state” variable (an integer)
• Assign each state in your FSM a unique integer value
• For each distinct state, we will have a separate piece of code that implements the transition function

• (we can also use an enumerated data type instead of integers)
FSMs in C

int state = 0;  // Initial state
while(1) {
    <do some processing of the sensory inputs>
    switch(state) {
    case 0:
        <handle state 0>
        break;
    case 1:
        <handle state 1>
        break;
    case 2: ...
    }
}
int state = 0;  // Initial state
while(1) {
    <do some processing of the sensory inputs>
    switch(state) {
        case 0:
            <handle state 0>
            break;
        case 1:
            <handle state 1>
            break;
        case 2: ... 
    }
}
int state = 0;  // Initial state
while(1) {
    <do some processing of the sensory inputs>
    switch(state) {
        case 0:
            <handle state 0>
            break;
        case 1:
            <handle state 1>
            break;
        case 2: ...
    }
}
FSMs in C

```c
int state = 0;  // Initial state
while(1) {
    //do some processing of the sensory inputs>
    switch(state) {
    case 0:
        <handle state 0>
        break;
    case 1:
        <handle state 1>
        break;
    case 2: ...
    }
}
```

Loop forever
FSMs in C

```c
int state = 0; // Initial state
while(1) {
    <do some processing of the sensory inputs>
    switch(state) {
        case 0:
            <handle state 0>
            break;
        case 1:
            <handle state 1>
            break;
        case 2: ... 
    }
}
```

“pseudo code”: not really code, but indicates what is to be done
FSMs in C

```c
int state = 0;  // Initial state
while(1) {
    // do some processing of the sensory inputs>
    switch(state) {
        case 0:
            // handle state 0
            break;
        case 1:
            // handle state 1
            break;
        case 2: ...
    }
}
```

In this case: we will translate the current sensory inputs into a representation of an event (if one has happened)
FSMs in C

```c
int state = 0;    // Initial state
while(1) {
    <do some processing of the sensory inputs>
    switch(state) {
        case 0:
            <handle state 0>
            break;
        case 1:
            <handle state 1>
            break;
        case 2: ...
    }
}
```

Switch/case syntax allows us to cleanly perform many “if(x==y)” operations.
FSMs in C

```c
int state = 0; // Initial state
while(1) {
    // do some processing of the sensory inputs>
    switch(state) {
    case 0:
        <handle state 0>
        break;
    case 1:
        <handle state 1>
        break;
    case 2: ...
    }
}
```

If state==0, then execute the following code.
FSMs in C

```c
int state = 0;  // Initial state
while(1) {
    <do some processing of the sensory inputs>
    switch(state) {
        case 0:
            <handle state 0>
            break;
        case 1:
            <handle state 1>
            break;
        case 2: ...
    }
}
```

This code can be as complex as necessary
FSMs in C

int state = 0;  // Initial state
while(1) {
    <do some processing of the sensory inputs>
    switch(state) {
        case 0:
            <handle state 0>
            break;
        case 1:
            <handle state 1>
            break;
        case 2: ...
    }
}

break says to exit the switch (don’t forget it or strange things will happen!)
FSMs in C

```c
int state = 0; // Initial state
while(1) {
    // do some processing of the sensory inputs>
    switch(state) {
        case 0:
            <handle state 0>
            break;
        case 1:
            <handle state 1>
            break;
        case 2: ...
    }
}
```

If state==1, then ...
int state = 0; // Initial state
while(1) {
  <do some processing of the sensory inputs>
  switch(state) {
    case 0:
      <handle state 0>
      break;
    case 1:
      <handle state 1>
      break;
    case 2: ... 
  }
}
int state = 0;  // Initial state
while(1) {
    <do some processing of the sensory inputs>
    switch(state) {
        case 0:
            <handle state 0>
            break;
        case 1:
            <handle state 1>
            break;
        case 2: ...
    }
}
Last Time

• Finite state machines

• FSM implementations in C
Today

• More about FSMs in C
• Programming the Atmel Mega8
• Computer Architecture
Administrivia

• Lab 2:
  – Available on the website
  – Get started this week!

• Lab in general:
  – Make sure you clean up your workspace before you leave
  – Put used batteries back on the charger – not in the “charged” battery boxes
Administrivia

• Midterm:
  – 3 weeks from today
  – Will be allowed 1 double-sided page of notes
  – More details in 2 weeks

• Class Thursday:
  – Discussion of homework 2 (and any remaining questions about homework 1)
  – A couple more FSM examples (last opportunity before midterm)
FSMs in C

```c
int state = 0;  // Initial state
while(1) {
    <do some processing of the sensory inputs>
    switch(state) {
        case 0:
            <handle state 0>
            break;
        case 1:
            <handle state 1>
            break;
        case 2:  ...
    }
}
```
FSMs in C (some other possibilities)

int state = 0; // Initial state
while(1) {
    <do some processing of the sensory inputs>
    switch(state) {
        case 0:
            <handle state 0>
            break;
        :;
        default:
            <handle default case>
            break;
    }
    <do some low-level control>
}
FSMs in C (some other possibilities)

```c
int state = 0;  // Initial state
while(1) {
    <do some processing of the sensory inputs>
    switch(state) {
        case 0:
            <handle state 0>
            break;
        default:
            <handle default case>
            break;
    }
    <do some low-level control>
}
```

Matches any state (if we reach this point)
FSMs in C (some other possibilities)

```c
int state = 0;  // Initial state
while(1) {
    <do some processing of the sensory inputs>
    switch(state) {
        case 0:
            <handle state 0>
            break;
        :
        default:
            <handle default case>
            break;
    }
    <do some low-level control>
}
```
(possibly) alter some control outputs (e.g., steering direction)
FSMs in C: Processing for Individual States

case STATE_10cents:
  // $.10 has already been deposited
  switch(event) {
    case EVENT_NICKEL: // Nickel
      state = STATE_15cents; // Transition to $.15
      break;
    case EVENT_DIME: // Dime
      state = STATE_20cents; // Transition to $.2
      break;
    case EVENT_JOLT: // Select Jolt
    case EVENT_BUZZ: // Select Buzzwater
      display_NOT_ENOUGH();
      break;

    case EVENT_NONE: // No event
      break; // Do nothing
  }
  break;
FSMs in C: Processing for Individual States

case 2:
    // $.10 has already been deposited
    switch(event) {
        case 0:   // Nickel
            state = 3;  // Transition to $.15
            break;
        case 1:   // Dime
            state = 4;  // Transition to $.2
            break;
        case 2:   // Select Jolt
        case 3:   // Select Buzzwater
        case 4:   // No event
            break; // Do nothing
    }

};
FSMs in C: Processing for Individual States

case 2:
   // $.10 has already been deposited
   switch(event) {
      case 0:   // Nickel
         state = 3;  // Transition to $.15
         break;
      case 1:   // Dime
         state = 4;  // Transition to $.2
         break;
      case 2:   // Select Jolt
      case 3:   // Select Buzzwater
      case 4:   // No event
         break;    // Do nothing
   }
   break;

A nickel has been received
FSMs in C: Processing for Individual States

case 2:
    // $.10 has already been deposited
    switch(event) {
        case 0:  // Nickel
            state = 3;  // Transition to $.15
            break;
        case 1:  // Dime
            state = 4;  // Transition to $.2
            break;
        case 2:  // Select Jolt
        case 3:  // Select Buzzwater
        case 4:  // No event
            break;    // Do nothing
    }
};
break;
FSMs in C: Processing for Individual States

case 2:
  // $.10 has already been deposited
  switch(event) {
  case 0:  // Nickel
    state = 3;  // Transition to $.15
    break;
  case 1:  // Dime
    state = 4;  // Transition to $.2
    break;
  case 2:  // Select Jolt
  case 3:  // Select Buzzwater
  case 4:  // No event
    break;    // Do nothing
  }
};
break;

If any of these match, then execute the following code (which does nothing in this example)
A Note on “Style” in C

• The numbers that we assigned to the different states are arbitrary (and at first glance, hard to interpret)
• Instead, we can define constant strings that have some meaning

• Replace: 0, 1, 2, 3, 4, 5
• With: STATE_00, STATE_05, STATE_10, STATE_15, STATE_20
A Note on “Style” in C

In C, this is done by adding some definitions to the beginning of your program (either in the .c file or the .h file):

```c
#define STATE_00   0
#define STATE_05   1
#define STATE_10   2
#define STATE_15   3
#define STATE_20   4
```
Atmel Mega8 Basics

- Complete, standalone computer
- Ours is a 28-pin package
- Most pins:
  - Are used for input/output
  - How they are used is configurable

<table>
<thead>
<tr>
<th>Pin</th>
<th>Description</th>
<th>Pin</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>(RESET) PC6</td>
<td>28</td>
<td>PC5 (ADC5/SCL)</td>
</tr>
<tr>
<td>2</td>
<td>(RXD) PD0</td>
<td>27</td>
<td>PC4 (ADC4/SDA)</td>
</tr>
<tr>
<td>3</td>
<td>(TXD) PD1</td>
<td>26</td>
<td>PC3 (ADC3)</td>
</tr>
<tr>
<td>4</td>
<td>(INT0) PD2</td>
<td>25</td>
<td>PC2 (ADC2)</td>
</tr>
<tr>
<td>5</td>
<td>(INT1) PD3</td>
<td>24</td>
<td>PC1 (ADC1)</td>
</tr>
<tr>
<td>6</td>
<td>(XCK/T0) PD4</td>
<td>23</td>
<td>PC0 (ADC0)</td>
</tr>
<tr>
<td>7</td>
<td>VCC</td>
<td>22</td>
<td>GND</td>
</tr>
<tr>
<td>8</td>
<td>GND</td>
<td>21</td>
<td>AREF</td>
</tr>
<tr>
<td>9</td>
<td>(XTAL1/TOSC1) PB6</td>
<td>20</td>
<td>AVCC</td>
</tr>
<tr>
<td>10</td>
<td>(XTAL2/TOSC2) PB7</td>
<td>19</td>
<td>PB5 (SCK)</td>
</tr>
<tr>
<td>11</td>
<td>(T1) PD5</td>
<td>18</td>
<td>PB4 (MISO)</td>
</tr>
<tr>
<td>12</td>
<td>(AIN0) PD6</td>
<td>17</td>
<td>PB3 (MOSI/OC2)</td>
</tr>
<tr>
<td>13</td>
<td>(AIN1) PD7</td>
<td>16</td>
<td>PB2 (SS/OC1B)</td>
</tr>
<tr>
<td>14</td>
<td>(ICP1) PB0</td>
<td>15</td>
<td>PB1 (OC1A)</td>
</tr>
</tbody>
</table>

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Atmel Mega8 Basics

Power (we will use +5V)
Atmel Mega8 Basics

Ground

PDIP

(RESET) PC6  1
(RXD) PD0  2
(TXD) PD1  3
(INT0) PD2  4
(INT1) PD3  5
(XCK/T0) PD4  6
VCC  7
GND  8

22  GND

21  AREF
20  AVCC
19  PB5 (SCK)
18  PB4 (MISO)
17  PB3 (MOSI/OC2)
16  PB2 (SS/OC1B)
15  PB1 (OC1A)
14  ICP1
13  AIN1
12  PD6
11  T1
10  XTAL2/TOSC2
9   PB6
8   XTAL1/TOSC1
7   VCC
6   PD4
5   INT1
4   INT0
3   TXD
2   RXD
1   RESET

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Atmel Mega8 Basics

Reset

- Bring low to reset the processor
- In general, we will tie this pin to high through a pull-up resistor (10K ohm)
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PORT B

(RESET) PC6 □ 1  28 □ PC5 (ADC5/SCL)
(RXD) PD0 □ 2  27 □ PC4 (ADC4/SDA)
(TXD) PD1 □ 3  26 □ PC3 (ADC3)
(INT0) PD2 □ 4  25 □ PC2 (ADC2)
(INT1) PD3 □ 5  24 □ PC1 (ADC1)
(XCK/T0) PD4 □ 6  23 □ PC0 (ADC0)
VCC □ 7  22 □ GND
GND □ 8  21 □ AREF

(XTAL1/TOSC1) PB6 □ 9  20 □ AVCC
(XTAL2/TOSC2) PB7 □ 10  19 □ PB5 (SCK)
(T1) PD5 □ 11  18 □ PB4 (MISO)
(AIN0) PD6 □ 12  17 □ PB3 (MOSI/OC2)
(AIN1) PD7 □ 13  16 □ PB2 (SS/OC1B)
(ICP) PB0 □ 14  15 □ PB1 (OC1A)
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PORT C

- (RESET) PC6 □ 1
- (RXD) PD0 □ 2
- (TXD) PD1 □ 3
- (INT0) PD2 □ 4
- (INT1) PD3 □ 5
- (XCK/T0) PD4 □ 6
- VCC □ 7
- GND □ 8
- (XTAL1/TOSC1) PB6 □ 9
- (XTAL2/TOSC2) PB7 □ 10
- (T1) PD5 □ 11
- (AIN0) PD6 □ 12
- (AIN1) PD7 □ 13
- (ICP1) PB0 □ 14
- 28 □ PC5 (ADC5/SCL)
- 27 □ PC4 (ADC4/SDA)
- 26 □ PC3 (ADC3)
- 25 □ PC2 (ADC2)
- 24 □ PC1 (ADC1)
- 23 □ PC0 (ADC0)
- 22 □ GND
- 21 □ AREF
- 20 □ AVCC
- 19 □ PB5 (SCK)
- 18 □ PB4 (MISO)
- 17 □ PB3 (MOSI/OC2)
- 16 □ PB2 (SS/OC1B)
- 15 □ PB1 (OC1A)
Atmel Mega8 Basics

PORT D

PDIP

(RESET) PD6 1
(RXD) PD0 2
(TXD) PD1 3
(INT0) PD2 4
(INT1) PD3 5
(XCK/T0) PD4 6
VCC 7
GND 8
(XTAL1/TOSC1) PB6 9
(XTAL2/TOSC2) PB7 10
(T1) PD5 11
(AIN0) PD6 12
(AIN1) PD7 13
(ICP1) PB0 14

28 PC5 (ADC5/SCL)
27 PC4 (ADC4/SDA)
26 PC3 (ADC3)
25 PC2 (ADC2)
24 PC1 (ADC1)
23 PC0 (ADC0)
22 GND
21 AREF
20 AVCC
19 PB5 (SCK)
18 PB4 (MISO)
17 PB3 (MOSI/OC2)
16 PB2 (SS/OC1B)
15 PB1 (OC1A)
A First Circuit
A First Program

Flash the LEDs at a regular interval

• How do we do this?
A First Program

How do we flash the LEDs at a regular interval?

- We toggle the state of PB0
main() {
    DDRB = 0xFF;  // Set all port B pins as outputs

    while(1) {
        PORTB = PORTB ^ 0x1;  // XOR bit 0 with 1
        delay_ms(500);  // Pause for 500 msec
        PORTB = PORTB ^ 0x2;  // XOR bit 1 with 1
        delay_ms(250);
        PORTB = PORTB ^ 0x2;  // XOR bit 1 with 1
        delay_ms(250);
    }
}
A First Program

main() {
    DDRB = 0xFF;  // Set all port B pins as outputs

    while(1) {
        PORTB = PORTB ^ 0x1;  // XOR bit 0 with 1
        delay_ms(500);        // Pause for 500 msec
        PORTB = PORTB ^ 0x2;  // XOR bit 1 with 1
        delay_ms(250);
        PORTB = PORTB ^ 0x2;  // XOR bit 1 with 1
        delay_ms(250);
    }
}

A predefined “variable” that controls whether the port B pins are digital inputs or outputs (more on this later)
A First Program

main() {
    DDRB = 0xFF; // Set all port B pins as outputs

    while(1) {
        PORTB = PORTB ^ 0x1; // XOR bit 0 with 1
        delay_ms(500); // Pause for 500 msec
        PORTB = PORTB ^ 0x2; // XOR bit 1 with 1
        delay_ms(250);
        PORTB = PORTB ^ 0x2; // XOR bit 1 with 1
        delay_ms(250);
    }
}

Loop forever
main() {
    DDRB = 0xFF;   // Set all port B pins as outputs

    while(1) {
        PORTB = PORTB ^ 0x1;   // XOR bit 0 with 1
        delay_ms(500);   // Pause for 500 msec
        PORTB = PORTB ^ 0x2;   // XOR bit 1 with 1
        delay_ms(250);
        PORTB = PORTB ^ 0x2;   // XOR bit 1 with 1
        delay_ms(250);
    }
}
A First Program

main() {
    DDRB = 0xFF;  // Set all port B pins as outputs

    while(1) {
        PORTB = PORTB ^ 0x1;  // XOR bit 0 with 1
        delay_ms(500);  // Pause for 500 msec
        PORTB = PORTB ^ 0x2;  // XOR bit 1 with 1
        delay_ms(250);
        PORTB = PORTB ^ 0x2;  // XOR bit 1 with 1
        delay_ms(250);
    }
}

Change the value being written to port B
A First Program

```c
main() {
    DDRB = 0xFF;   // Set all port B pins as outputs
    while(1) {
        PORTB = PORTB ^ 0x1;   // XOR bit 0 with 1
        delay_ms(500);         // Pause for 500 msec
        PORTB = PORTB ^ 0x2;   // XOR bit 1 with 1
        delay_ms(250);
        PORTB = PORTB ^ 0x2;   // XOR bit 1 with 1
        delay_ms(250);
    }
}
```

Bit-wise XOR operator
A First Program

main() {
    DDRB = 0xFF;   // Set all port B pins as outputs

    while(1) {
        PORTB = PORTB ^ 0x1;   // XOR bit 0 with 1
        delay_ms(500);   // Pause for 500 msec
        PORTB = PORTB ^ 0x2;   // XOR bit 1 with 1
        delay_ms(250);
        PORTB = PORTB ^ 0x2;   // XOR bit 1 with 1
        delay_ms(250);
    }
}

Program pauses for 500 msec. This function is defined elsewhere in this sample program.
A First Program

```c
main() {
    DDRB = 0xFF;   // Set all port B pins as outputs

    while(1) {
        PORTB = PORTB ^ 0x1;   // XOR bit 0 with 1
        delay_ms(500);          // Pause for 500 msec
        PORTB = PORTB ^ 0x2;   // XOR bit 1 with 1
        delay_ms(250);
        PORTB = PORTB ^ 0x2;   // XOR bit 1 with 1
        delay_ms(250);
    }
}
```

What does this program do?
A First Program

```c
main() {
  DDRB = 0xFF;  // Set all port B pins as outputs

  while(1) {
    PORTB = PORTB ^ 0x1;  // XOR bit 0 with 1
    delay_ms(500);  // Pause for 500 msec
    PORTB = PORTB ^ 0x2;  // XOR bit 1 with 1
    delay_ms(250);
    PORTB = PORTB ^ 0x2;  // XOR bit 1 with 1
    delay_ms(250);
  }
}
```

**Flashes LED on PB1 at 2 Hz on PB0: 1 Hz**
Last Time

- FSM: abstract and in C
- Atmel basics
Today

• Atmel essentials:
  – Digital I/O
  – Care and feeding
  – Basic circuits
  – Cross-compiler use
  – Downloading code

• FSM examples

• HW 2
Bit Masking

• Suppose we have a 3-bit number (so values 0 … 7)
• Suppose we want to set the state of B3, B4, and B5 with this number (B3 is the least significant bit)

• How do we express this in code?
Bit Masking in Practice

• Suppose you have connected your 3 robot control lines to B3, B4, and B5.
• Suppose also that you have connected your 2 turret control lines to B6 and B7

• Our robot control lines are specified as numbers 0 … 6
• How do we change the state of B3…B5 without changing the turret command?
Bit Masking

main() {
    DDRB = 0xF8;  // Set pins B3, B4, B5, B6, B7 as outputs

    unsigned short val;  // A short is 8-bits wide

    val = command_to_robot;

    PORTB = (PORTB & 0xC7)            // Set the current B3-B5 to 0s
    | ((val & 0x7)<<3);            // OR with new values
    (shifted
    // to fit within B3-B5
}
Bit Masking

```c
main() {
    DDRB = 0xF8;   // Set pins B3, B4, B5, B6, B7 as outputs

    :

    :

    unsigned short val;  // A short is 8-bits wide

    val = command_to_robot;

    PORTB = (PORTB & 0xC7)        // Set the current B3-B5 to 0s
                      | ((val & 0x7))<<3);  // OR with new values
                      (shifted
                      // to fit within B3-B5)
}

B3-B7 are outputs; all others are still inputs (could be
different depending on how other pins are used)
```
Bit Masking

main() {
    DDRB = 0xF8;   // Set pins B3, B4, B5, B6, B7 as outputs
    :
    :

    unsigned short val;  // A short is 8-bits wide

    val = foobar;

    PORTB = (PORTB & 0xC7)        // Set the current B3-B5 to 0s
        | ((val & 0x7))<<3);       // OR with new values
        // to fit within B3-B5

    "Mask out" the current values of pins B3-B5 (leave everything else intact)
Bit Masking

```c
main() {
    DDRB = 0xF8;   // Set pins B3, B4, B5, B6, B7 as outputs

    :
    :

    unsigned short val;  // A short is 8-bits wide

    val = foobar;

    PORTB = (PORTB & 0xC7)        // Set the current B3-B5 to 0s
        | ((val & 0x7))<<3);        // OR with new values
        // to fit within B3-B5
}
```

Substitute an arbitrary value into these bits
Bit Masking

```c
main() {
    DDRB = 0xF8;  // Set pins B3, B4, B5, B6, B7 as outputs

    :
    :

    unsigned short val;  // A short is 8-bits wide

    val = foobar;

    PORTB = (PORTB & 0xC7)       // Set the current B3-B5 to 0s
     | ((val & 0x7))<<3);         // OR with new values
    //     (shifted
    //      // to fit within B3-B5
}
```

And use the result to change the output state of port B
Reading the Digital State of Pins

```c
main() {
    DDRB = 0x38;   // Set pins B3, B4, B5 as outputs
    // All others are inputs (suppose we care
    // about bits B6 and B7 only (so a 2-bit
    // number)

    :
    :

    unsigned short val, outval;  // A short is 8-bits wide

    val = PINB;

    outval = (val & 0xC0) >> 6;
}
```

B6 and B7 are configured as inputs
Reading the Digital State of Pins

```c
main() {
    DDRB = 0x38;   // Set pins B3, B4, B5 as outputs
    // All others are inputs (suppose we care
    // about bits B6 and B7 only (so a 2-bit
    // number)

    :
    :

    unsigned short val, outval;  // A short is 8-bits wide
    val = PINB;

    outval = (val & 0xC0) >> 6;
}

Read the value from the port
```
Reading the Digital State of Pins

```c
main() {
    DDRB = 0x38; // Set pins B3, B4, B5 as outputs
    // All others are inputs (suppose we care
    // about bits B6 and B7 only (so a 2-bit
    // number)

    unsigned short val, outval; // A short is 8-bits wide

    val = PINB;

    outval = (val & 0xC0) >> 6;
}

“Mask out” all bits except B6 and B7
```
Reading the Digital State of Pins

main() {
    DDRB = 0x38;  // Set pins B3, B4, B5 as outputs
    // All others are inputs (suppose we care
    // about bits B6 and B7 only (so a 2-bit
    // number)
    :
    :

    unsigned short val, outval;  // A short is 8-bits wide

    val = PINB;

    outval = (val & 0xC0) >> 6;
}

Right shift the result by 6 bits – so the value of B6
and B7 are now in bits 0 and 1 of "outval"
Port-related “Variables”

The set of C-accessible variables (actually called registers) for controlling digital I/O:

<table>
<thead>
<tr>
<th></th>
<th>Directional control</th>
<th>Reading</th>
<th>Writing</th>
</tr>
</thead>
<tbody>
<tr>
<td>Port B</td>
<td>DDRB</td>
<td>PORTB</td>
<td>PINB</td>
</tr>
<tr>
<td>Port C</td>
<td>DDRC</td>
<td>PORTC</td>
<td>PINC</td>
</tr>
<tr>
<td>Port D</td>
<td>DDRD</td>
<td>PORTD</td>
<td>PIND</td>
</tr>
</tbody>
</table>
Putting It All Together

• Program development:
  – On your own laptop
  – We will use a C “crosscompiler” (avr-gcc and other tools) to generate code on your laptop for the mega8 processor

• Program download:
  – We will use “in circuit programming”: you will be able to program the chip without removing it from your circuit
Physical Interface for Programming

AVR ISP
Physical Interface for Programming

AVR ISP

Serial connection to your computer
Physical Interface for Programming

AVR ISP

Header connection will connect to your circuit (through an adapter)
A More Complicated Circuit
A More Complicated Circuit

- Through adapter to AVR ISP
- Do not reverse the pins!
A More Complicated Circuit

Extra LED allows you to see when a program is being downloaded.
A More Complicated Circuit

16 MHz crystal
- Optional!
- Without it, your processor will run at 1MHz (ok for now)
Compiling and Downloading Code

- Create a “makefile” and a C source file in some personal directory (start with copies from the “Atmel HOWTO” part of the web page
- Change the TARGET line in makefile to match the name of your source file
- Change your source file
Compiling and Downloading Code

• From the windoze menu: select “Start” and then “run”
• Type “cmd”. This will bring up a “shell” interface (a command line)
• “cd” (change directory) to your directory
• Type “make”. Deal with any errors or warnings (both are there for a reason)
• Type “make program”
Compiling and Downloading Code

• Once the chip is programmed, the AVR ISP will automatically reset the processor; starting your program
Hints

• Use LEDs to show status information (e.g., the state of your FSM)
• Have one LED blink in some unique way at the beginning of your program
• Go slow:
  – Implement and test incrementally
  – Insert plenty of pauses into your code (use delay_ms())
Final Notes

• You will need some additional circuitry to program your processor
• There are a few more details in the code

-> See the examples posted on the net
Next Time

• Procedure for programming and downloading to the Mega8
• Inside the Mega8